

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	35	(US-20040210860-\$ or US-20050166167-\$ or US-20030225552-\$ or US-20040019468-\$ or US-20050149837-\$ or US-20050262456-\$ or US-20050240885-\$ or US-20050171747-\$ or US-20050192789-\$ or US-20050155002-\$ or US-20040103378-\$ or US-20050216871-\$ or US-20040123254-\$ or US-20040153983-\$ or US-20040093572-\$ or US-20020178424-\$ or US-20060212837-\$ or US-20040153308-\$ or US-20050210433-\$ or US-20050114809-\$ or US-20050086648-\$).did. or (US-7203917-\$ or US-5469367-\$ or US-6415430-\$ or US-6442732-\$ or US-6496961-\$ or US-6473884-\$ or US-6944838-\$ or US-5953519-\$ or US-6643827-\$ or US-6957404-\$ or US-6728939-\$ or US-5331568-\$).did. or (EP-1515251-\$).did. or (JP-2004005674-\$).did.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/13 15:33
L2	0	1 AND ((immediate ADJ2 (implication implies imply)) (((failure ADJ2 driven) failure\$1driven) ADJ2 assertion) FDAs)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/13 15:34
S1	0	((design model abstraction abstract abstracting abstracted circuit system Dynamic) with (verifi\$6 verify\$3 validat\$3 test\$3 "logic checking" logic\$1checking (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co\$1verify\$3 (co adj validat\$3) co\$1validat\$3)) SAME (unroll unrolled unrolling un\$1roll un\$1rolled un\$1rolling) SAME (satisfiability SAT) SAME Lazy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/01 15:10
S2	0	((design model abstraction abstract abstracting abstracted circuit system Dynamic) with (verifi\$6 verify\$3 validat\$3 test\$3 "logic checking" logic\$1checking (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co\$1verify\$3 (co adj validat\$3) co\$1validat\$3)) SAME (unroll unrolled unrolling un\$1roll un\$1rolled un\$1rolling) SAME (satisfiability SAT) AND Lazy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/01 15:12
S4	7	Aarti.in. Gupta.in. Malay.in. Ganai.in. Zijiang.in. Yang.in. Pranav.in. Ashar.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/05/01 15:18
S5	4	((design model abstraction abstract abstracting abstracted circuit system Dynamic) with (verifi\$6 verify\$3 validat\$3 test\$3 "logic checking" logic\$1checking (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co\$1verify\$3 (co adj validat\$3) co\$1validat\$3)) AND (unroll unrolled unrolling un\$1roll un\$1rolled un\$1rolling) SAME (satisfiability SAT) AND Lazy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/01 15:21
S6	4	("5469367" "6415430" "6442732" "6496961").PN. OR ("7203917").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/05/01 15:23
S7	0	S6 AND (unroll unrolled unrolling un\$1roll un\$1rolled un\$1rolling) AND (satisfiability SAT) AND Lazy	US-PGPUB; USPAT; USOCR	OR	ON	2007/05/01 15:23
S8	34	(US-20040093571-\$ or US-20050081169-\$ or US-20050262456-\$ or US-20030225552-\$ or US-20040230407-\$ or US-20050240885-\$ or US-20050071147-\$ or US-20050131665-\$ or US-20040078674-\$ or US-20050166167-\$ or US-20030208730-\$).did. or (US-6957404-\$ or US-6581026-\$ or US-6609229-\$ or US-6728939-\$ or US-6141630-\$ or US-6816825-\$ or US-6985840-\$ or US-6745160-\$ or US-6885983-\$ or US-6192505-\$ or US-6175946-\$ or US-6728665-\$ or US-7007249-\$ or US-6848088-\$ or US-6311293-\$ or US-6102959-\$ or US-5465216-\$ or US-5661661-\$ or US-6292765-\$ or US-6944838-\$ or US-5724504-\$ or US-6356858-\$ or US-6651234-\$).did.	US-PGPUB; USPAT	OR	ON	2007/05/10 12:56

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S9	1	(verifi\$6 verify\$3 validat\$3 test\$3 "logic checking" logic\$1checking (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co\$1verify\$3 (co adj validat\$3) co\$1validat\$3) WITH (satisfiability SAT) WITH (BMC "bounded model checking" bounded\$1model\$1checking) WITH (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 13:20
S10	8	(verifi\$6 verify\$3 validat\$3 test\$3 "logic checking" logic\$1checking (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co\$1verify\$3 (co adj validat\$3) co\$1validat\$3) SAME (satisfiability SAT) SAME (BMC "bounded model checking" bounded\$1model\$1checking) SAME (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 13:32
S11	4	S8 S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/05/10 13:37
S12	24	((("20050216871") or ("20050192789") or ("20050171747") or ("20050155002") or ("20040123254") or ("20040103378") or ("20040093572") or ("20020178424") or ("6473884") or ("20040230407") or ("20040210860") or ("20040153983"))).PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:45
S13	7	(S8 S10 S12) AND ((satisfiability SAT) WITH (BMC "bounded model checking" bounded\$1model\$1checking) WITH proof)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 14:46
S14	15	((("6957404") or ("6643827") or ("20050210433") or ("20050114809") or ("20050086648") or ("20040153308") or ("6728939") or ("5953519"))).PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/10 14:45
S15	0	S14 AND ((satisfiability SAT) WITH (BMC "bounded model checking" bounded\$1model\$1checking) WITH proof)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 14:55
S16	34	(US-20040210860-\$ or US-20050166167-\$ or US-20030225552-\$ or US-20040019468-\$ or US-20050149837-\$ or US-20050262456-\$ or US-20050240885-\$ or US-20050171747-\$ or US-20050192789-\$ or US-20050155002-\$ or US-20040103378-\$ or US-20050216871-\$ or US-20040123254-\$ or US-20040153983-\$ or US-20040093572-\$ or US-20020178424-\$ or US-20060212837-\$ or US-20040153308-\$ or US-20050210433-\$ or US-20050114809-\$ or US-20050086648-\$).did. or (US-7203917-\$ or US-5469367-\$ or US-6415430-\$ or US-6442732-\$ or US-6496961-\$ or US-6473884-\$ or US-6944838-\$ or US-5953519-\$ or US-6643827-\$ or US-6957404-\$ or US-6728939-\$).did. or (EP-1515251-\$).did. or (JP-2004005674-\$).did.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/10 14:55

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S17	10	S16 AND ((satisfiability SAT) SAME (BMC "bounded model checking" bounded\$1model\$1checking) SAME proof)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 14:59
S18	6	("20020138812" "20030225552" "20040019468" "5440568" "6698003" "6848088").PN. OR ("6944838").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/05/10 14:58
S19	2	S18 AND ((satisfiability SAT) SAME (BMC "bounded model checking" bounded\$1model\$1checking) SAME proof)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:23
S20	2	(S14 S16 S18) AND (Lazy WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:26
S21	40	(S14 S16 S18) AND (((design model circuit) with (abstract abstracted abstraction abstracting reduce reduced reducing reduction decrease decreased decreasing shrink shrinking shrunk condense condensed condensing compress compressed compressing compression contract contracted contracting simplify simplified simplifying simplification drop dropped dropping transform transformed transforming transformation)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:30
S22	8	(S14 S16 S18) AND (((design model circuit) with (abstract abstracted abstraction abstracting reduce reduced reducing reduction decrease decreased decreasing shrink shrinking shrunk condense condensed condensing compress compressed compressing compression contract contracted contracting simplify simplified simplifying simplification drop dropped dropping transform transformed transforming transformation)) WITH (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:42
S23	0	(S14 S16 S18) AND ((design model circuit) with (partition cluster dividing divide division subdividing subdivide subdivision "sub dividing" "sub divide" "sub division" separation separate separating) WITH (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:44
S24	2	(S14 S16 S18) AND ((design model circuit) SAME (partition cluster dividing divide division subdividing subdivide subdivision "sub dividing" "sub divide" "sub division" separation separate separating) SAME (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:57
S25	20	(S14 S16 S18) AND (design model circuit) AND (partition cluster dividing divide division subdividing subdivide subdivision "sub dividing" "sub divide" "sub division" separation separate separating) AND (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:46
S26	1	(S14 S16 S18) AND ((interface interfaced interfacing connection connecting connect connected link linked linking linkage boundary boundaries interaction interactions interacted terminal) ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 15:58

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S27	1	(S14 S16 S18) AND ((external outside outward outer) ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 16:01
S28	0	(S14 S16 S18) AND (Lazy ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:36
S29	2	(S14 S16 S18) AND (Lazy WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 16:10
S30	4	(S14 S16 S18) AND (temporal WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:36
S31	4	(S14 S16 S18) AND diver	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 16:46
S32	4	("5469367" "6415430" "6442732" "6496961").PN. OR ("7203917").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/05/10 16:15
S33	1	"Property Checking via Structural Analysis"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:20
S36	22	(Gupta.in. Ganai.in. Yang.in. Ashar.in.) AND (satisfiability SAT) AND (BMC "bounded model checking" bounded\$1model\$1checking)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:23
S37	5	(Gupta.in. Ganai.in. Yang.in. Ashar.in.) AND diver AND (satisfiability SAT) AND (BMC "bounded model checking" bounded\$1model\$1checking)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:23
S38	0	(10/157486).APP.	USPAT; USOCR	OR	ON	2007/05/10 17:25
S39	1	S36 AND (Lazy ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:36

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S40	2	S36 AND (temporal WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/10 17:36
S41	2	("5331568").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/11 23:04
S42	35	(US-20040210860-\$ or US-20050166167-\$ or US-20030225552-\$ or US-20040019468-\$ or US-20050149837-\$ or US-20050262456-\$ or US-20050240885-\$ or US-20050171747-\$ or US-20050192789-\$ or US-20050155002-\$ or US-20040103378-\$ or US-20050216871-\$ or US-20040123254-\$ or US-20040153983-\$ or US-20040093572-\$ or US-20020178424-\$ or US-20060212837-\$ or US-20040153308-\$ or US-20050210433-\$ or US-20050114809-\$ or US-20050086648-\$).did. or (US-7203917-\$ or US-5469367-\$ or US-6415430-\$ or US-6442732-\$ or US-6496961-\$ or US-6473884-\$ or US-6944838-\$ or US-5953519-\$ or US-6643827-\$ or US-6957404-\$ or US-6728939-\$ or US-5331568-\$).did. or (EP-1515251-\$).did. or (JP-2004005674-\$).did.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/12 00:25
S43	0	S42 AND ((immediate ADJ2 implication) immediate\$1implication)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/12 00:27
S44	12	S42 AND implication	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/12 00:27
S45	6	S42 AND (clause implication)	US-PGPUB; USPAT; EPO; JPO	WITH	ON	2007/05/12 00:29
S46	0	S42 AND (clause WITH (dummy dumb))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2007/05/12 00:29